Some future RPCEmu thoughts

Things we'd like to change (in no particular order)

Outline

- Accuracy (TLB, Timers, Other O.S.s)
- Networking
- Testing
- DynaRec

Accuracy – TLB

- What is the TLB?

- TLB on RPCEmu

- Problems with TLB implementation

- Planned changes

- Current status of changes

Accuracy – Timers

- What timers does a Risc PC contain?

- How the timers are used

- Timers on RPCEmu

- Planned changes

- Current status of changes

Accuracy – Other Operating Systems

- Other Operating Systems for Risc PC

- Current compatibility

Networking

- Current situation
    - Windows
    - Linux
- Problems with current networking
- Proposal
- Slirp
- Benefits
- Current status

Testing

- Automated testing

- RISC OS booting

- Demo (RISC OS booting test)

- ARM testing

- Demo (ARM testing)

- Planned changes

Dynamic Recompiler (DynaRec)

- How DynaRec works

- Problems

- Potential improvements

- Current status

- Demo